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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No
Priority Filing Date
Inventor
Assignee Micron Technology, Inc.
Assignee
Priority Examiner B. Abbott
Attorney's Docket No
TITLE: Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods
of Forming Floating Gate Transistors, and Floating Gate Transistors

Assistant Commissioner for Patent BEST AVAILABLE COPY Washington, D. C. 20231

Attention: Official Draftsman

SUBSTITUTE DRAWING REQUEST

Please enter the enclosed substitute drawing in the above-referenced application in place of the drawing originally filed. A Red-line drawing of Fig. 8 is submitted herewith along with the corrected substitute drawing.

Acknowledgment of receipt of the formal drawing and acceptance into the file is requested.

Respectfully submitted,

Date: 120,2000

By:

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Enclosures: 1 Sheet of Red-line drawing, Fig. 8, and 1 Sheet of Formal Drawing, Fig. 8.